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BIBDATASHEET

CONFIRMATION NO. 3437

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:	SERIAL NUMBE 10/786,296	FILING OR 371(c) DATE 02/26/2004 RULE	CLASS 257		OUP ART UNIT 2811		ATTORNEY DOCKET NO. 103213-00072	
	APPLICANTS							
	Nobuhiro Nishikawa, Kyoto-shi, JAPAN; Koichi Inoue, Kyoto-shi, JAPAN;							
	** CONTINUING DATA **********************************							
	** FOREIGN APPI JAPAN 200							
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	Foreign Priority claimed	d	0TA TE 0D	OUEETO	WING CLAIMS		INDEPENDENT	
	35 USC 119 (a-d) cond met Verified and Acknowledged	Allowance	ter STATE OR COUNTRY JAPAN	DRAWING 2			CLAIMS 2	
	ADDRESS							
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12/2/04	Washington ,DC 20036-5339							
	TITLE Semiconductor integrated circuit device configured to prevent the generation of a reverse current in a MOS transistor							
		-		□ AI	☐ All Fees			
					1.16 Fees (Filing)			
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		EES: Authority has been gi lo. to charge/cre	ven in Paper edit DEPOSIT ACCOU		DI			
		No for following:			☐ 1.18 Fees (Issue)			
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					☐ Credit			